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JP0932 U.S. PTO
09/737540
12/16/88

	Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. TR SCANNED 883 Q.A. 1A H/S	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/737540	F	257	751	2811	KANG

APPLICANTS

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Kung-Hyon Nam

TITLE

Semiconductor device wiring and method of manufacturing the same

PTO-2040
12/89

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL CLASSIFICATION									
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
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